

Customer No.: 31561  
Application No.: 10/604,773  
Docket No.: 8615-US-PA-1

### REMARKS

#### Present Status of the Application

It is noted with great appreciation that the Examiner indicated Claims 7-9 are allowable over prior art of record.

Claims 1-9 are pending. For at least the following reason, Applicants respectfully submit that claims 1-9 patently define over prior art of record and reconsideration of this application is respectfully requested.

#### Interview Summary

The undersigned would like to thank Examiner Toledo for granting a telephonic interview on January 18, 2005, during which the 35 U.S.C. 102(e) rejection in the Office Action dated December 14, 2004 was discussed. More particularly, the undersigned and the Examiner discussed the rejections and the teachings of the Ozaki reference. The undersigned pointed out to Examiner Toledo that the openings 154 of Ozaki do not expose the data distribution line or the so-called drain bus line 101. Instead, the opening 154 only forms beside the data distribution line 101. Examiner Toledo agreed and further indicated a notice of allowance would be issued unless other issues were identified.

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**Discussion of the claim rejection under 35 USC 102**

*The Office Action rejected claims 1-6 under 35 USC 102(e) as being anticipated by Ozaki et al. (US-2002/0131010, hereinafter Ozaki).*

*In rejecting the above claims, the Examiner stated that Ozaki discloses a pixel structure including a thin film transistor 121, a pixel electrode 113, a scan line 103, a data distributing line 101, a repairing conductive line 115 underneath the data distributing line 101, and a second dielectric layer 143 covering the data distributing line 101, wherein the repairing conductive line 115 has a connective section and repair sections at each end thereof, and the repair sections cover an area larger than the data distributing line (FIG. 27B); forming a first opening 154 and a second opening 154 in the first and second dielectric layers so that a portion of the data distributing line 101 and the respective repair sections of the repairing conductive line are exposed (??) (FIG. 27B); and forming a conductive layer 156 over the exposed surface of the first opening and the second openings 154 so that the respective repair sections (of the repairing conductive line 115) and the data distributing line 101 are electrically connected (??) (FIG. 27B).*

Applicant respectfully disagrees and would like to particularly point out that the FIG. 27B clearly shows that the first and second openings 154 expose only portions of the repair conductive line 115 (in fact the capacitor bus line) and the data distributing line 101 is covered by the second dielectric layer 143, and therefore by forming a conductive layer 156 in the first and second openings 154, the repairing conductive line

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115 (in fact the capacitor bus line) and the data distributing line 101 cannot be electrically connected with each other because the data distributing line 101 is covered by the second dielectric layer 143.

In other words, Ozaki substantially fails to teach or disclose at least the steps of forming a first opening and a second opening in the first dielectric layer and the second dielectric layer above the repair sections of the repairing conductive line so that a portion of the data distributing line and the respective repair sections of the repairing conductive line are exposed; and forming a conductive layer over the exposed surface of the first opening and the second opening so that the respective repair sections of the repairing conductive line and the data distributing line are electrically connected, as required by the proposed claim 1.

Therefore, Ozaki fails to teach or disclose every features of the proposed Claim 1 in this regard.

Claims 2-6, which directly or indirectly depend from claim 1, are also patentable over Ozaki at least because of their dependency from an allowable base claim.

For at least the above reasons, it is therefore submitted that claims 1-6 patently define over Ozaki and therefore claims 1-6 should be allowed. Reconsideration and withdrawal of these rejections is respectfully requested.

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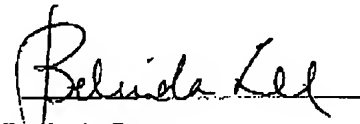
### CONCLUSION

For at least the foregoing reasons, it is believed that all the pending claims 1-9 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date :

February 4, 2005

Respectfully submitted

  
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